REMARKS

Claims 1, 2, 4-7, 9, 10, 12, 13 and 15 are pending in the application. Claims 1 and 13 have been amended. New claim 17 has been added. Examiner's reconsideration of the rejections in view of the above amendments and following remarks is respectfully requested.

Claim Objections

Claim 13 has been amended above, and the status identifier of claim has been changed to "Currently Amended".

Claim Rejections - 35 U.S.C. § 103

Claims 1-2, 4-7, 8-10, 12-13 and 15 stand rejected as being unpatentable over U.S. Patent No. 6,073,232 to Kroeker in view of U.S. Patent No. 6,434,695 to Esfahani, et al. At the very least, it is respectfully submitted that claims 1, 10 and 13 are patentable and non-obvious over the combination of Kroeker and Esfahani.

For instance, with respect to claim 1, the combination of Kroeker and Esfahani does *not* disclose or suggest *preloading the boot data into a cache memory prior to completion of initialization of a central processing unit of the computer system, wherein preloading the boot data comprises accessing compressed boot data from a boot device,* as essentially recited in claim 1. Although Esfahani arguably discloses loading compressed boot data into a RAM cache, it is submitted that Esfahani does not cure the deficiencies of Kroeker in that Esfahani does not disclose *preloading the boot data into a cache memory prior to completion of initialization of a central processing unit of the computer system.* Indeed, Esfahani specifically discloses (Col. 8, line 40, through Col. 9,

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line 6, for example) that a *Boot Info file* (40) is located and loaded into *RAM* (12) *after completion of initialization of the computer system*, to begin booting the operating system. The is in stark contrast to the claimed *preloading the boot data into a cache memory prior to completion of initialization of a central processing unit of the computer system*, as claimed in claim 1.

Further, with respect to claim 10, the combination of Kroeker and Esfahani does not teach or suggest *preloading the application data into a cache memory upon launching the application program, wherein preloading the application data comprises accessing compressed application data from a persistent storage device*, as essentially recited in claim 10. Indeed, both Kroeker and Esfahani are directed to booting operating systems. In contrast, the claimed invention is directed to a method for accelerated loading of an application program, which is not taught by Kroeker and Esfahani, alone or in combination.

Moreover, with respect to claim 13, the combination of Kroeker and Esfahani does not teach or suggest, e.g., *a programmable volatile logic device, wherein the programmable volatile logic device is programmed by the DSP or controller prior to completion of initialization of a central processing unit of the host system*, much less *the DSP or controller preloading the compressed boot data into the cache memory device prior to completion of initialization of the central processing unit of the host system, and decompressing the preloaded compressed boot data to service requests for boot data from the host system after completion of initialization of the central processing unit of the host system*, as essentially recited in claim 13.

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Therefore, claims 1, 10 and 13 are patentable and non-obvious over the combination of Kroeker and Esfahani. Moreover, the remaining dependent claims are patentable over the cited combination at least by virtue of their dependence from respective base claims 1, 10 or 13.

Early and favorable consideration by the Examiner is respectfully urged. Should the Examiner believe that a telephone or personal interview may facilitate resolution of any remaining matters, it is requested that the Examiner contact Applicants' undersigned attorney.

Respectfully submitted,

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